## Please amend the second line of the table on page 6 as follows:

## Aa

	Analog circuit	Digital circuit
The number	Few (about 20 pcs in	Many (2000 pcs in
of	multiplier)	8-bit multiplier)
transistors		
Chip area	Small (few devices)	Large (many
		devices)
Power	Low power	Large (many gates
consumption	consumption because	are switched)
	of fewer devices	
Clock	Low (determined by	Higher (1/2 of cut-
frequency	settling of	off frequency of
	amplifier)	device)
Signal	High (about 1/2 of	Low (1/10 of clock
frequency	cut-off frequency of	frequency)
	device)	
Precision	Low (device	High (depending on
	deviation, noise)	bit number)
Stability	Low (oscillation,	High
	characteristic	
	variation)	
Noise	Low (S/N)	Strong (large noise
resistance		margin)

Please amend the paragraph bridging pages 26-27 of the specification as follows:

A3

The gas flow detection circuit 10 outputs a voltage signal representing a gas flow passing through a gas passage. The gas flow detection circuit 10 may be a gas flow detection circuit DECT1 shown in Fig. 25 which detects a current flowing through a resistor arranged in the gas passage or a voltage across the resistor and outputs a voltage signal representing the gas flow passing through the gas passage.

Please amend the equation at line 10 of page 44 of the specification as follows:

A4

 $Dout = (a \cdot Dtemp + b) \cdot Din + (c \cdot Dtemp + d)$  (6)